



Modified Form PTO/SB/08A

Substitute for form 1449A/PTO				<b>Complete If Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				Application Number	09/846,490
				Filing Date	April 30, 2001
				First Named Inventor	Benjamin Eldridge et al.
				Group Art Unit	3729
				Examiner Name	C. Arbes
Sheet	2	of	2	Attorney Docket No.	P6D2-US

OTHER PRIOR ART – NONPATENT LITERATURE DOCUMENTS			
Examiner Initials <sup>*</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
Cp	18	Lee, "Design and Fabrication of a monolithic High-Density Probe Card for High-Frequency on-Wafer Testing", IEEE, IEDM 89-289-292, (1989).	

Examiner Signature	C. J. Arbes	Date Considered	4/15/05
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication

<sup>1</sup>Unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.



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U.S. PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant	Publication Date	Related	Copy Enclosed
		Number	Kind Code <sup>2</sup>				
CA	1	3493858		D.G. Baron et al.	2/3/70		No
CA	2	3806801		Bove	4/23/74		No
CA	3	3911361		Bove et al.	10/7/75		No
DA	4	4423376		Brynes et al.	12/27/83		No
CA	5	4837622		Whann et al.	6/6/89		No
CA	6	4916002		Carver	4/10/90		No
CA	7	5059898		Barsotti et al.	10/22/91		No
CA	8	5177439		Liu et al.	1/5/93		No
CA	9	5221415		Albrecht et al.	6/22/93		No
CA	10	5367165		Toda et al.	11/22/94		No
CA	11	5546375		Shimada et al.	8/13/96		No

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant	Publication Date	Copy Enclosed	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup>				
CA	12	JP	1150862		Mitsubishi Electric Corp.	6/13/89	Yes	X
CA	13	JP	6-151532		Tokyo Electron Yamanashi KK	5/31/94	<del>Yes</del> Abs only	X
CA	14	JP	63-293934		Hitachi	11/30/88	Yes	X
CA	15	JP	6-84455		Canon Kabushiki Kaisha	3/25/94	Yes	X
CA	16	JP	7-7052		Seiko-Epson Corp	1/10/95	Yes	X
CA	17	EP	508707		Texas Instruments	10/14/92	Yes	X

Examiner Signature	<i>C. J. Arbes</i>	Date Considered	<i>9/15/05</i>
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		Number	Kind Code <sup>2</sup>			
ga	1	3,963,986		Morton et al.	6/15/76	

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup>				
cja	2	KR	10-0013608		Tokyo Electron Co.	9/6/90		

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cja	3	Korea Institute of Machinery & Materials "Development of Manufacturing Process for Membrane Probe Card", Ministry of Science and Technology, pp.1-62, (1992).	

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